Attorney's Docket No .:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :

Yamazaki, et al.

Serial No.:

09/898,986

Filed

July 3, 2001

Title

SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING

Art Unit: 2813

Examiner : Laura Schillinger

THE SAME

Commissioner for Patents Washington, D.C. 20231

TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total

Claims

63 7

31

32

\$576

Independent

4

3

\$252

Information Disclosure Statement

\$180

TOTAL FEE DUE

\$1,008

A check for \$1,008 is attached.

Respectfully submitted,

Xeg. No. 33,945

Fish & Richardson P.C.

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL I hereby certify under 37 CFR \$1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231. 11/14/2002

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RESPONSE

In response to the action mailed August 14, 2002, please amend the application as follows:

In the claims:

Please amend claims 1, 2, 6, 7, and 11 as follows:

--1. (Amended) A method of manufacturing a semiconductor device, comprising the steps of:

forming first and second semiconductor islands on an insulating surface;

introducing ions of a p-type impurity into at least a portion of only said first semiconductor island without mass separation wherein said portion is to become a channel region of a thin film transistor; and

11/22/2002 HDEMESS1 00000059 09898986

01 FC:1201 02 FC:1202 252.00 OP 576.00 OP

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1/14/2002

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